

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,910,944 B2  
APPLICATION NO. : 09/863118  
DATED : June 28, 2005  
INVENTOR(S) : Allan Gleason, Manoocher Birang and William L. Guthrie

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page Item (56), Other Publications; Replace:

“Anonymous, Endpoint Detection of Oxide Polishing and Planarization of Semiconductor Devices,” with  
-- Anonymous, “Endpoint Detection of Oxide Polishing and Planarization of Semiconductor Devices”, --

In Column 23, Claim 24, Line 49; Replace:

“The polishing pad of claim 21, wherein article” with  
-- The polishing pad of claim 21, wherein the article --

In Column 24, Claim 33, Line 22; Replace:

“The polishing pad of claim 31,” with  
-- The polishing pad of claim 32, --

Signed and Sealed this

Twenty-first Day of November, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*